



DEC 27 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
M. Takeuchi
Serial No.: 097930786
Filed: August 15, 2001
For: METHODS FOR MANUFACTURING
SEMICONDUCTOR DEVICES AND
SEMICONDUCTOR DEVICES HAVING
TRENCH ISOLATION REGIONS

Examiner: Vu

Art Unit: 2811



24033

PATENT TRADEMARK OFFICE

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ASSISTANT COMMISSIONER OF PATENTS
Washington, D.C. 20231

Sir:

Transmitted herewith in the above-identified application is an:

- ☒ Amendment 8 pages.
☒ Petition for Extension of Time.
☐ Transmittal of Formal Drawings and sheets of formal drawings.
☐ No additional fee is required.
☒ Return Postcard.

The fee has been calculated as shown below:

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TOTAL	31	MINUS 31	=	0	x	\$0	OR x 18 \$0
INDEP CLAIMS	4	MINUS 5	=	0	x	\$0	OR x 84 \$0
<u> </u> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM				+	\$	OR + 280 \$	
				TOTAL		\$0	OR TOTAL \$ -0-

 Please charge Deposit Account No. 50-0585 the amount of \$ to cover the extension fee and also the amount of \$ to cover the claim fee. A duplicate copy of this sheet is enclosed.

☒ A check in the amount of \$ 920 to cover the extension fee is enclosed.

 A check in the amount of \$ to cover the filing fee is enclosed.

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☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0585. A duplicate of this sheet is enclosed.

☒ Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

☒ Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

Alan S. Raynes
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Dated: December 17, 2002

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on December 17, 2002.

Alan S. Raynes
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12/17/02
Date